

What Is Claimed Is:

1. A method for adaptation of functions for controlling operating sequences, the functions accessing at least one global variable of at least one program for control and this global variable being assigned address information which is present in at least one memory means, this address information of the global variable being loaded by at least one load instruction out of the memory means, wherein the address information of the global variable of the load instruction is replaced.
2. The method as recited in Claim 1, wherein the address information of the global variable is replaced by the address information of a pointer variable.
3. The method as recited in Claim 2, wherein the address information of the pointer variable is located in a reserved memory area.
4. The method as recited in Claim 1, wherein a memory instruction is manipulated onto the global variable by replacing the memory instruction with a jump instruction.
5. The method as recited in Claim 1, wherein an initial address of the function is determined from the address information.
6. The method as recited in Claim 1 or 5, wherein the functions for controlling operating sequences are replaced by replacing the address information with additional functions.

7. The method as recited in Claim 4,
wherein the functions for controlling the operating
sequences by replacing the memory instruction with the
jump instruction are replaced with additional functions.
8. A device for implementing a method for adaptation of
functions for controlling operating sequences, the
functions accessing at least one global variable of at
least one program for control, and this global variable
being assigned address information, a memory means being
provided in which the address information is located,
control means also being provided through this address
information of the global variable is loaded out of the
memory means by at least one load instruction,
wherein the control means are designed in such a way that
the address information of the global variable of the
load instruction is replaced.
9. The device as recited in Claim 8 for performing a method
for adaptation of functions for controlling operating
sequences,
wherein the control means are designed in such a way that
a method as recited in one of Claims 2 through 7 is
implemented in addition.
10. A control unit for controlling operating sequences having
a device for implementing the method as recited in Claim
8.
11. A computer program product having program code stored on
a machine-readable medium, for implementing the method as
recited in one of Claims 1 through 7 when the program is
executed on a computer.

12. A computer program having program code for implementing all steps as recited in one of Claims 1 through 7 when the program is executed on a computer.